

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Huang, *et al.*

Attorney Docket: TSM03-0340

Filed: Herewith

Examiner: TBD

Serial No.: TBD

Art Unit: TBD

For: Integration Film Scheme for Copper / Low-k Interconnect

Mail Stop: Patent Application
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

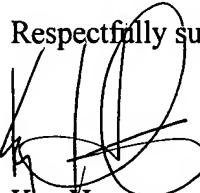
The Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

November 12, 2003

Date

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Respectfully submitted,

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Sheet

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| Application Number | TBD |
| Filing Date | Herewith |
| First Named Inventor | Huang, et al. |
| Art Unit | TBD |
| Examiner Name | TBD |

Attorney Docket Number TSM03-0340

U.S. PATENT DOCUMENTS

| Examiner Initials* | Cite No. ¹ | Document Number | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear |
|--------------------|-----------------------|--|--------------------------------|--|---|
| | | Number - Kind Code ² (if known) | | | |
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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

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| Examiner Initials* | Cite No. | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
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Examiner Signature

Date Considered

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